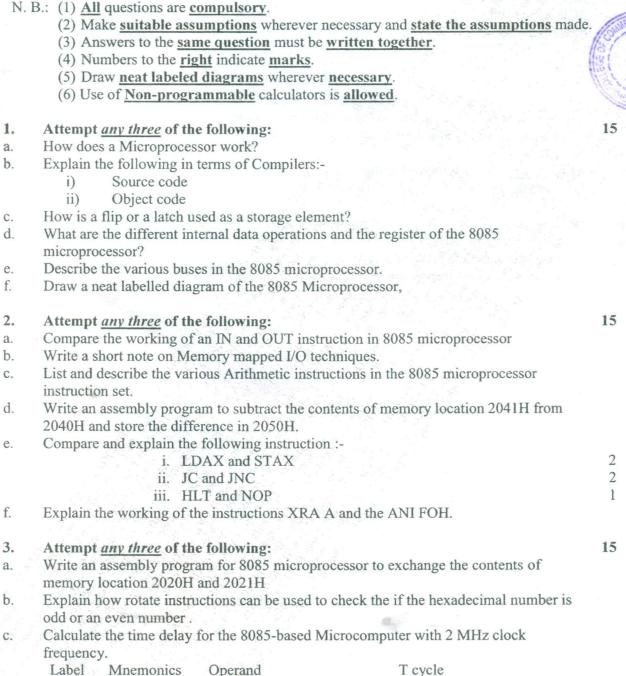
FYBSCIT sem II Regular & A.T.K.T Exam April- 2018 microprocessor Architecture Opcode. 33400

1614118

(2½ Hours)

[Total Marks: 75]



- LXI B, 2384H 10

 LOOP: DCX B 6

 MOV A,C 4

 ORA B 4

 JNZ LOOP 10/7
- d. Write a program to generate a Square wave of a 500 microsecond delay.
- e. Explain the effect of the POP and PUSH instruction on the Stack Pointer.
- f. List and describe the working of Various Calls and Returns instruction in 8085 microprocessor

		WAR COUNTY FIRM XATE FINES APVI	
		Q. P. Code: 33408	
	4.	Attempt <u>any three</u> of the following:	1:
3135	a.	Write an assembly program for 8085 microprocessor to convert (1111 1111) 2 to its BCD equivalent.	
Sales Sales	b.	Explain the following instruction for 8085 microprocessor: i) DAA ii) XCHG	
The same	c. d.	Explain the working of an interrupt in 8085 microprocessor. What is the function of an editor, assembler and loader?	
	e.	List and describe of files generated after cross assembling	
	f.	Write a short note on SIM instruction.	3
	5.	Attempt any three of the following:	1:
	a.	What are the features of Pentium Processor.	
	b.	List and describe the special Pentium registers	
	c.	Describe the memory management in Pentium and Pentium pro processors	
	d.	Compare Core i5 and i7 processors	
	e.	Describe the general SPARC Architecture:	
	f.	What are the various instruction format in the SPARC Architecture?	