

12/05/2022

F.Y.B.Sc.IT - Sem II - Reg. Exam - May '22

Microprocessor Architecture

12.5.22



MALINI KISHOR SANGHVI COLLEGE OF COMMERCE AND ECONOMICS

MICROPROCESSOR ARCHITECTURE

F.Y.B.Sc IT

SEMESTER II

**N.B: ALL QUESTIONS ARE COMPUSORY WITH INTERNAL CHOICE
FIGURES TO THE RIGHT INDICATE FULL MARKS.**

SECTION II

Q.2 ANSWER THE FOLLOWING (ANY TWO): 8

- A) Explain the types of computers languages
- B) Interface 2 K ROM and 2 K RAM with 8085 microprocessor
- C) Interface 1 K ROM and 2 K RAM with 8085 microprocessor
- D) Explain the process of testing.

Q.3 ANSWER THE FOLLOWING (ANY TWO): 8

- A) Write an 8085 assembly level program to subtract two numbers stored at C030 and C032. Store the result from C032.
- B) Write an 8085 assembly level program to divide two numbers stored at C030 and C031. Store the result from C032.
- C) Describe the various types of branch instructions.
- D) Describe logical instructions in 8085

Q.4 ANSWER THE FOLLOWING (ANY TWO): 8

- A) Explain compare instruction in 8085 with examples
- B) Describe the modulo Ten counters
- C) Describe the left rotate instructions of 8085 with examples
- D) Describe the conditional call instructions

Q.5 WRITE SHORT NOTES ON (ANY TWO): 8

- A) BCD to ASCII conversion
- B) Types of operating system
- C) Cross assemblers
- D) 8085 Vectored Interrupts

Q.6 WRITE SHORT NOTES ON (ANY TWO): 8

- A) Pentium II
- B) i3 processor
- C) i7 processor
- D) Instruction format of SUN SPARC microprocessor

XXX